

The diagram shows a signal flow from a **SIGNAL SOURCE @ $F_0/3$** (12) to an **X3 MULT** block (14), and then to a **MODULATOR** block (16). An **INPUT SIGNAL** is also fed into the **MODULATOR** block. The output of the **MODULATOR** is the **MODULATED SIGNAL @ F_0** . A reference numeral 10 points to the overall system.

**FIG. 1
(PRIOR ART)**

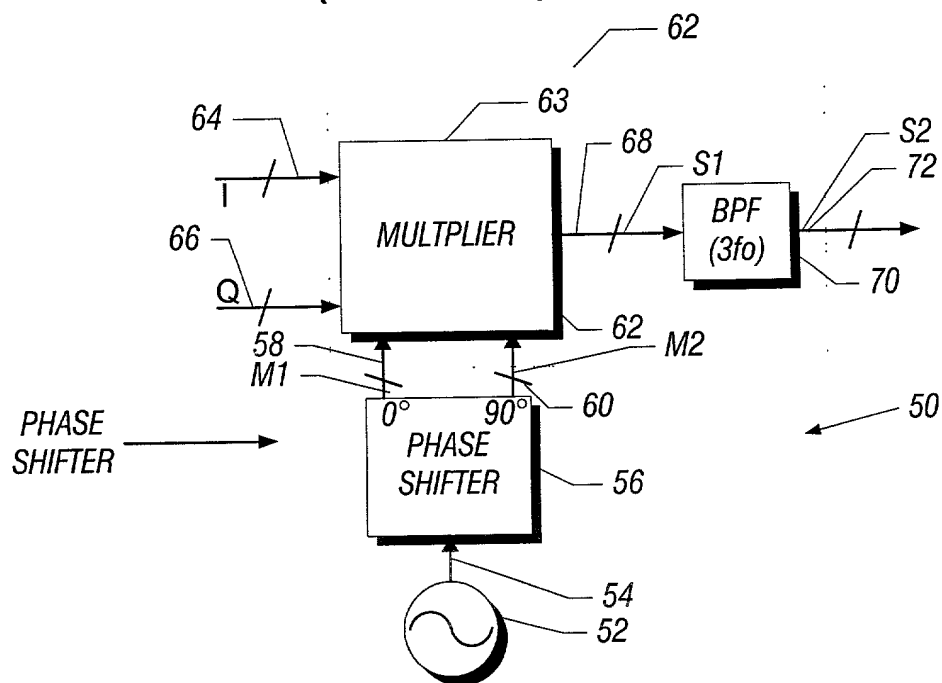
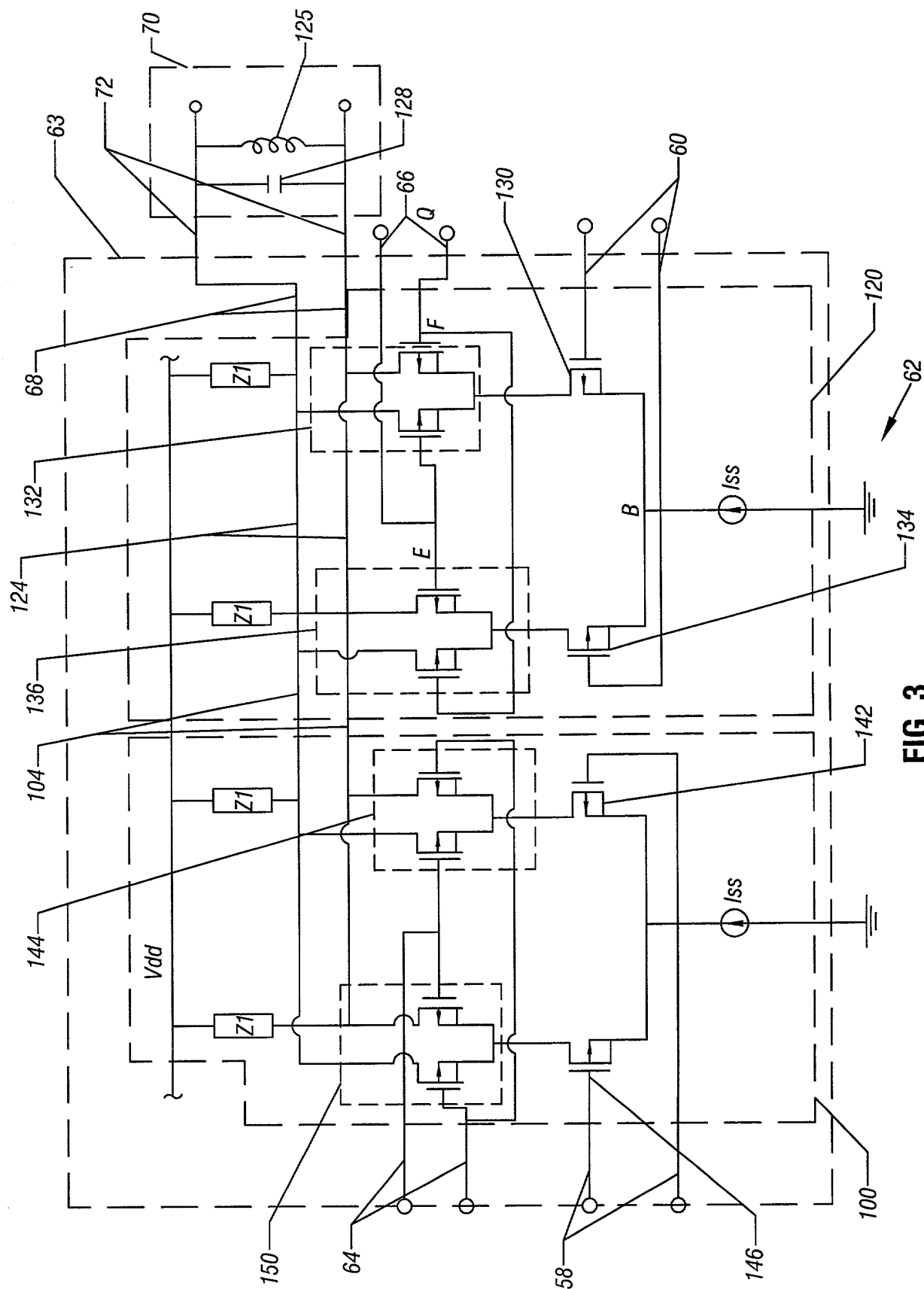


FIG. 2



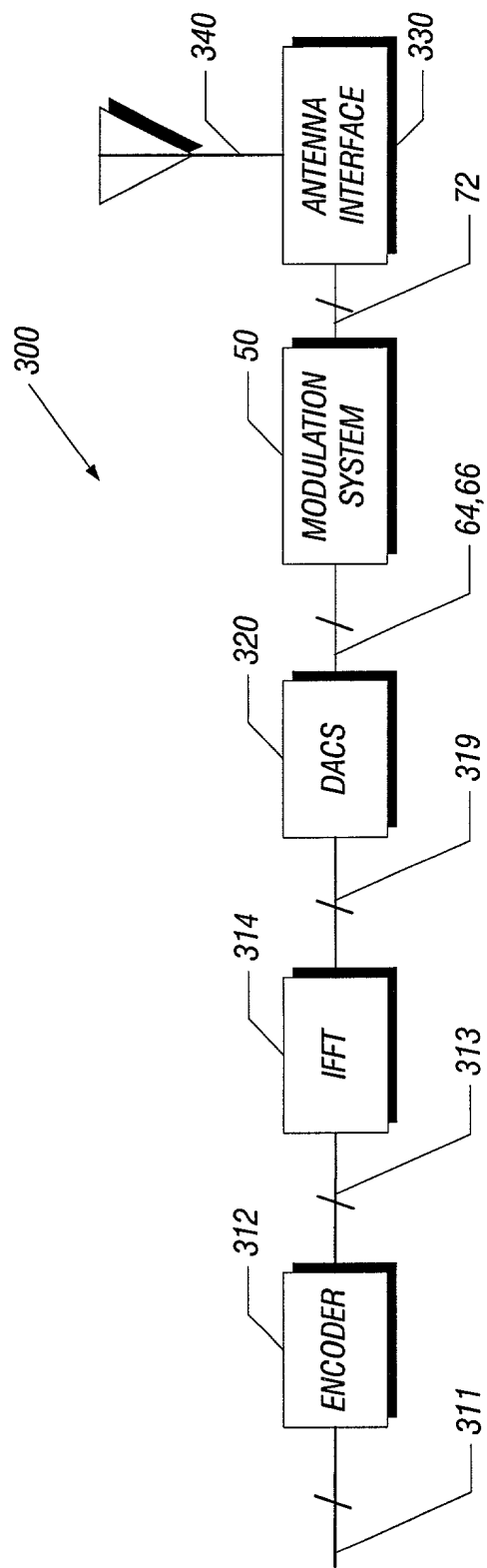


FIG. 4